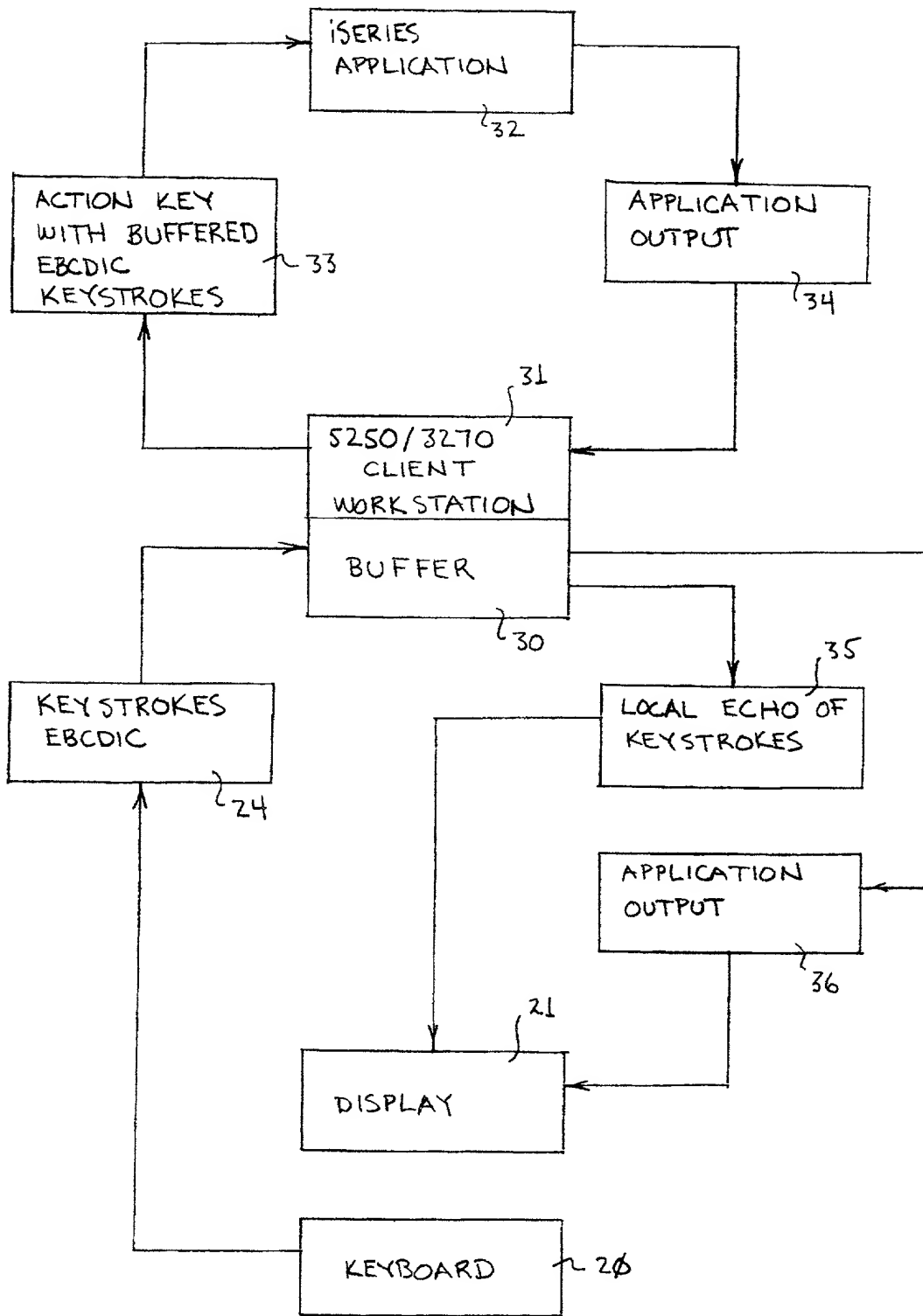


FULL DUPLEX CHARACTER MODE (VTxxx)

(PRIOR ART)

FIG. 1



HALF DUPLEX BLOCK MODE
(PRIOR ART)

FIG.2

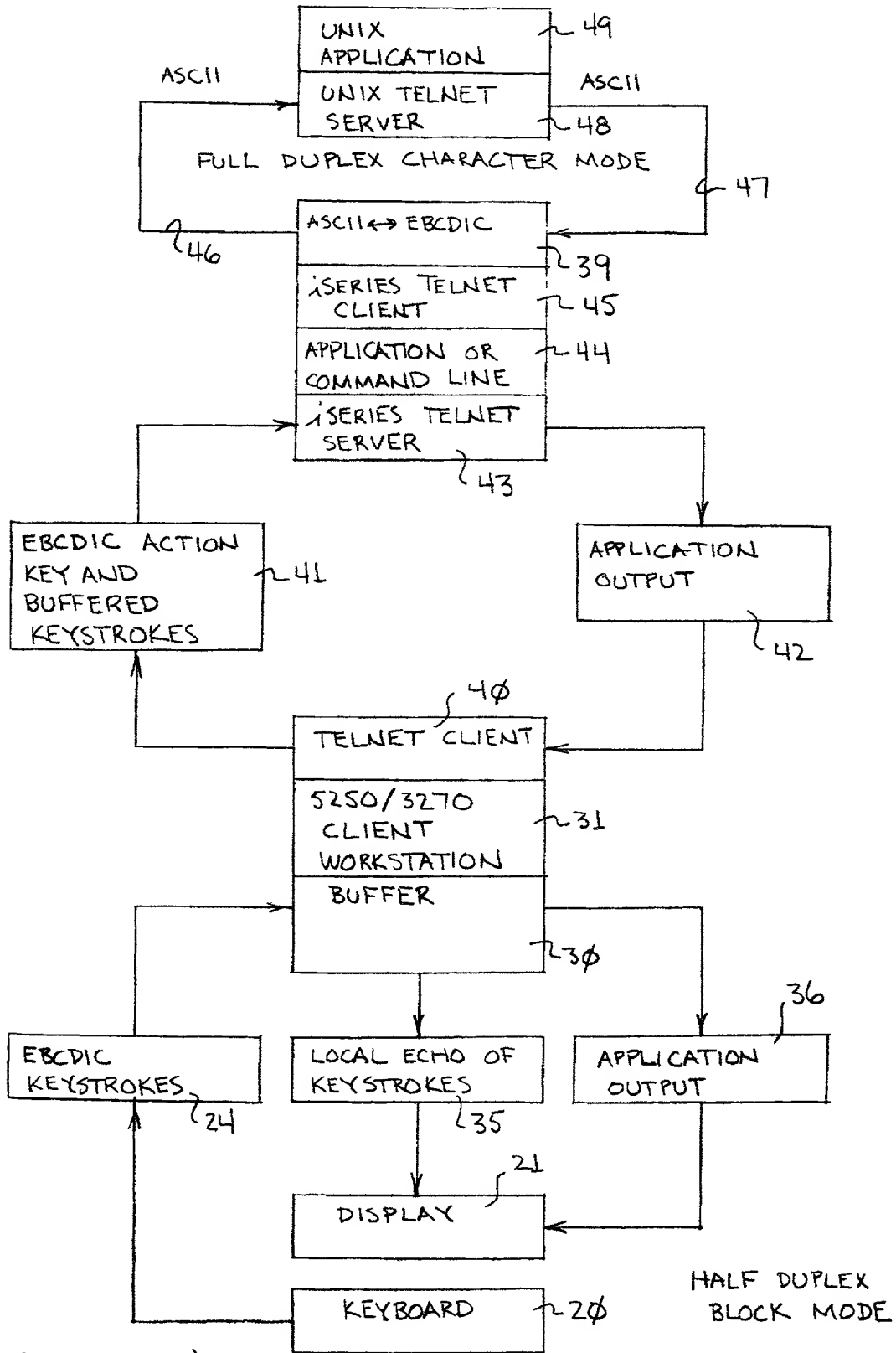
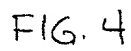


FIG. 3



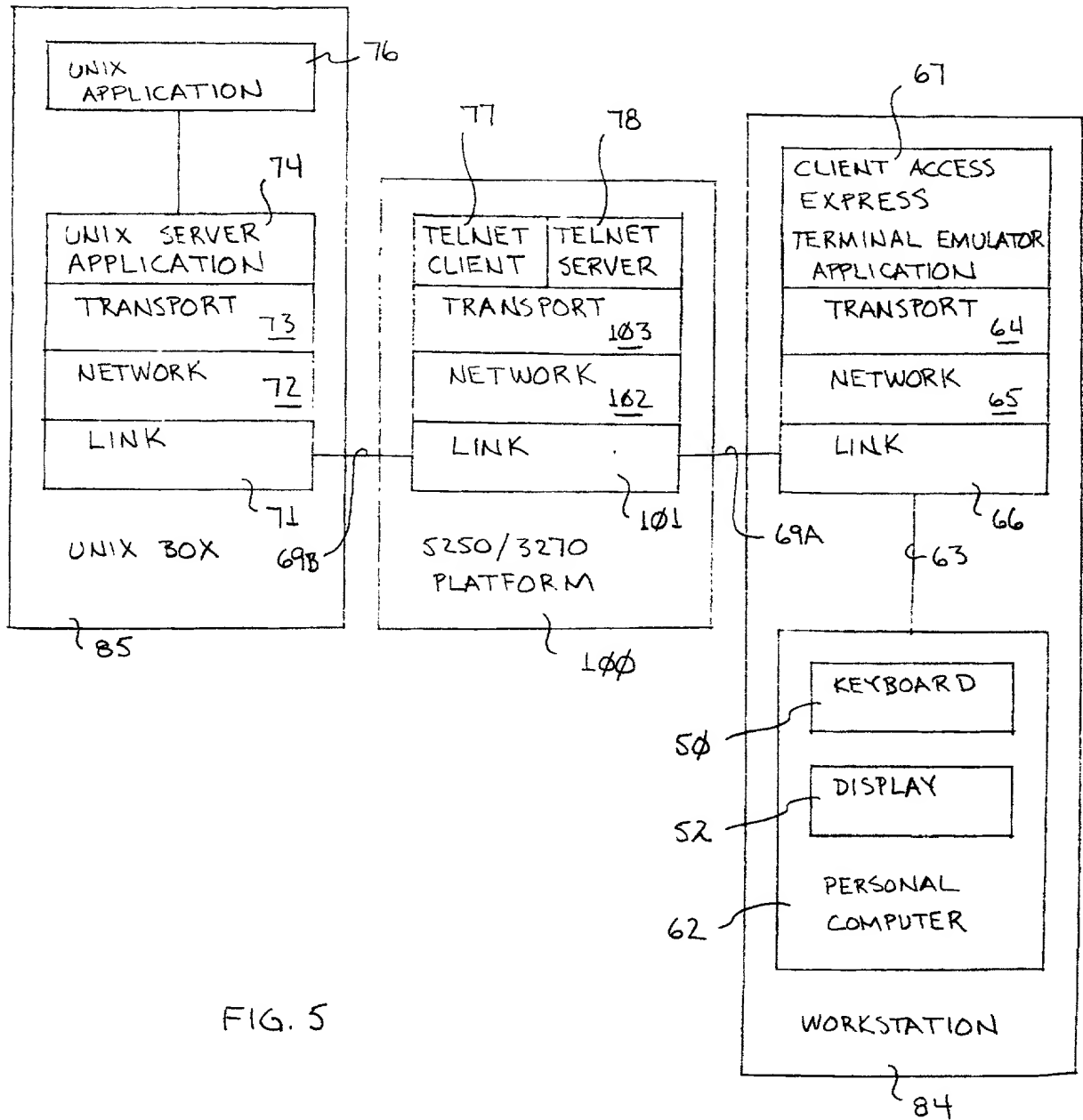


FIG. 5

80

EXAMPLE IMPLEMENTATION

FIG. 6